

Appl. No.: 10/678,595
Docket No.: DB000859-007
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Reply to Office action of 26 July 2004

Amendments to the Specification

On page 1, please amend paragraph 1 as follows:

[0001] The present invention is a continuation of U.S. Application Serial No. 09/792,554 entitled "Voltage Regulator and Data Path for a Memory Device" filed 23 February 2001, now abandoned, which is a divisional of U.S. Application Serial No. 09/654,099 entitled "Voltage Regulator and Data Path for a Memory Device" filed 31 August 2000, issued as U.S. Patent No. 6,545,929.

On page 13, please amend the Abstract as follows:

~~The present invention is directed to a~~ A method and apparatus for ~~providing~~ provide unbalanced output drive capability, for example, to correct for output skews in subsequent output stages. ~~One aspect of the present invention relates to~~ In one aspect, a pre-driver or the like ~~which~~ provides unbalanced output drive capability. The pre-driver is comprised of first and second data paths having a plurality of transistor output stages and a plurality of switches for controlling the conductivity of the plurality of output stages in response to the level of conductivity of a subsequent driver output stage. ~~Another aspect of the present invention relates to a~~ A method of correcting output skews in a subsequent amplification stage is also provided. ~~Other aspects of the present invention relate to~~ In another aspect, a portion of a data path, a memory device, and a computer system all ~~having~~ have a pre-driver with pre-driver output transistors responsive to signals indicative of the strength of output drive transistors.